Chart, funnel chart

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| --- | --- |
| Course Number | ELE404 |
| Course Title | Electronic Circuits I |
| Semester/Year | W2022 |
| Instructor | Fei Yuan |
| TA Name | Anas Razzaq |

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| --- | --- |
| Lab/Tutorial Report No. | Design Project |
|  |  |
| Report Title | BJT Amplifiers |

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| --- | --- |
| Section No. | 01 |
| Submission Date | 17-April-2022 |
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|  |  |  |
| --- | --- | --- |
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\*By signing above, you attest that you have contributed to this submission and confirm that all work you have contributed to this submission is your own work. Any suspicion of copying or plagiarism in this work will result in an investigation of Academic Misconduct and may result in a “0” on the work, an “F” in the course, or possibly more severe penalties, as well as a Disciplinary Notice on your academic record under the Student Code of Academic Conduct, which can be found online at:

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# Introduction

This report is for Design Project “BJT Amplifiers”. The project took place from 1 April 2022 to 17 April.

# Objectives

The objective of this lab was to design an amplifier circuit that complies with the following specifications.

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# Circuit Made

Diagram, schematic

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The Circuit shows 3 stage amplifiers CE then CE then CC Amplifiers.

First CE has an amplification of x2 second one has an amplification of x25.

CC has amplification of x1.

# 

# Results

Input:

Chart

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Output: 8V p2p without load

Chart, line chart

Description automatically generated

Output: more than 4 p2p with load

Chart, line chart

Description automatically generated

Av without load:

Vin = 0.075V

Vo= 4

Vin/Vo=53.333

Av with load:

Vin = 0.075V

Vo= 3.31

Vin/Vo= 44.1333

Timeline

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# Calculations

Text, letter

Description automatically generated

Text, letter

Description automatically generated

# Calculations

In conclusion, the circuit was mostly calculated except for the RC and RE in S2 these were picked based on trial and error. And the S3 circuit was based on the Lab 7 CC circuit with the resistor put as 1k to minimize clipping.